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BIBDATASHEET

CONFIRMATION NO. 9619

Bib Data Sheet

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|-----------------------------|-----------------------------------|--------------|------------------------|--------------------------------------|
| SERIAL NUMBER 10/600,568 | FILING DATE 06/23/2003 RULE | CLASS 257 | GROUP ART UNIT 2814 | ATTORNEY DOCKET NO. 04329.2444-01 |
|-----------------------------|-----------------------------------|--------------|------------------------|--------------------------------------|

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** CONTINUING DATA *****

This application is a DIV of 09/677,743 10/03/2000 PAT 6,611,060

** FOREIGN APPLICATIONS *****

JAPAN 11-282404 10/04/1999
 JAPAN 11-282405 10/04/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 09/12/2003

| Foreign Priority claimed 35 USC 119 (a-d) conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR COUNTRY JAPAN | SHEETS DRAWING 8 | TOTAL CLAIMS 12 | INDEPENDENT CLAIMS 1 |
|---|---|------------------------------|------------------------|-----------------------|----------------------------|
| Verified and Acknowledged Examiner's Signature | | Initials | | | |

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 filed 4/9/04

TITLE

Method of manufacturing semiconductor device and semiconductor device

including Two-step Polishing Operation for
 cap metal

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